



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants: LEUNG et al.

Serial No. 10/666,399

Filed: September 18, 2003

Title: MOLDED CHIP FABRICATION METHOD AND APPARATUS

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

INFORMATION DISCLOSURE STATEMENT UNDER 37 C.F.R. §1.97(b)

Sir:

Pursuant to 37 C.F.R. §§ 1.56 and 1.97(b), applicants bring to the attention of the Examiner the documents listed on the attached Form PTO-1449. This Information Disclosure Statement is being filed within three months of the filing of the above referenced application or before the mailing date of a first Office Action on the merits for the above-referenced application.

In accordance with the PTO procedure, (all patent applications filed after June 30, 2003), we have not enclosed the U.S. patent references cited on PTO form 1449.

Applicants respectfully request that the Examiner consider the listed documents and evidence that consideration of relevant portions thereof by making appropriate notations on the attached form.

This submission does not represent that a search has been made or that no better art exists and does not constitute an admission that each or all of the listed documents are material or constitute "prior art". If the Examiner applies any of the documents as prior art against any claim in the application and applicants determine that the cited documents do not

CERTIFICATE OF MAILING

I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450 on

11/5/03
Date


Marianne Middleton

constitute "prior art" under United States law, applicants reserve the right to present to the Office the relevant facts and law regarding the appropriate status of such documents.

Applicants further reserve the right to take appropriate action to establish the patentability of the disclosed invention over the listed documents, should one or more of the documents be applied against the claims of the present application.

Respectfully submitted,

Dated: 11/5/03



Jaye G. Heybl
Registration No. 42,661
Attorney for Applicant

KOPPEL, JACOBS, PATRICK & HEYBL
555 St. Charles Drive, Suite 107
Thousand Oaks, California 91360
(805) 373-0060
IDS-1.97(b).doc

FORM PTO-1449 (Modified)

INFORMATION DISCLOSURE CITATION
IN AN APPLICATION

(Use several sheets if necessary)

Docket No.
P0298US-7Application Number
10/666,399Applicant
LEUNG et al.Filing Date
September 18, 2003

Group Art Unit

U.S. PATENT DOCUMENTS

Examiner Initial	Document Number							Date	Name	Class	Subclass	Filing Date If Appropriate
	5	9	5	9	3	1	6	09/1999	LOWERY	257	98	
	6	4	0	4	1	2	5	06/2002	GARBUZOV et al.	313	499	
	5	9	8	8	9	2	5	12/1999	SHIMIZU et al.	313	503	
	5	8	1	3	7	5	3	09/1998	VRIENS et al.	362	293	
	6	0	6	6	8	6	1	05/2000	HOHN et al.	257	99	

FOREIGN PATENT DOCUMENTS

	Document Number							Date	Country	Class	Subclass	Translation	
												Yes	No
	EP	1	98	0	1	6	A2	4/17/02	EUROPE			X	

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)

	1	NICHIA CORP. WHITE LED, PART NOS. NSPW300BS, NSPW312BS, p. 1-3, 2000.
	2	PATENT APPLICATION PUBLICATION US 2002/0185965 A1, DEC. 12, 2002, COLLINS, III et al.
	3	PATENT APPLICATION PUBLICATION US 2002/0070449 A1, JUNE 13, 2002, YAGI et al.
	4	NPO-30394, ELECTROPHORETIC DEPOSITION FOR FABRICATING MICROBATTERIES, p. 1-2, NASA TECH BRIEFS ISSUE. MAY 3, 2003.
Examiner		Date Considered
EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through		